

**POWER OF ATTORNEY
and
CORRESPONDENCE ADDRESS
INDICATION FORM**

Application Number	08/453,732
Filing Date	May 30, 1995
First Named Inventor	Ronald T. Fulks
Title	METHOD OF MANUFACTURING ACTIVE MATRIX LCD USING FIVE MASKS
Art Unit	2515
Examiner Name	TON, MINH TOAN T
Attorney Docket Number	XR940013

I hereby appoint:

☐ Practitioners at Customer Number**OR**☐ Practitioner(s) named below:

Name	Registration Number

as my/our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the Patent and Trademark Office connected therewith.

Please recognize or change the correspondence address for the above-identified application to:

☐ The above-mentioned Customer Number:**OR**☒ The address associated with Customer Number:**OR**

24498

☐ Firm or
Individual Name

Address

Address

City

State

ZIP

Country

Telephone

Fax

I am the:

☐ Applicant/Inventor.☒ Assignee of record of the entire interest. See 37 CFR 3.71.

Certificate under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

SIGNATURE of Applicant or Assignee of RecordName **Richard LaPeruta (Reg. No. 51252)**Signature *Richard LaPeruta*

Date Jul. 8, 2010

Telephone

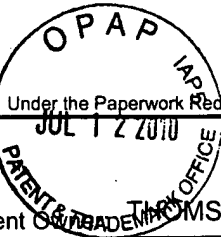
609-734-6816

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

☒ *Total of 2 forms are submitted.

This collection of information is required by 37 CFR 1.31 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: THOMSON LICENSING Customer No. 24498 Docket No: XR940013
Application No./Patent No.: Appln. 08/453,732; Pat. 5,621,556 Filed/Issue Date: Filed May 30, 1995; Issued Apr. 15, 1997

Titled: METHOD OF MANUFACTURING ACTIVE MATRIX LCD USING FIVE MASKS

THOMSON LICENSING, a Corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest in;
2. ☐ an assignee of less than the entire right, title, and interest in
(The extent (by percentage) of its ownership interest is _____ %); or
3. ☐ the assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made)

the patent application/patent identified above, by virtue of either:

A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy therefore is attached.

OR

B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: Fulks, R.T.; Yao, W; Tsai, C.C. To: Xerox Corp.

The document was recorded in the United States Patent and Trademark Office at
Reel 007661, Frame 0877, or for which a copy thereof is attached.

2. From: Xerox Corp. To: Bank One, NA

The document was recorded in the United States Patent and Trademark Office at
Reel 013153, Frame 0001, or for which a copy thereof is attached.

3. From: Bank One, NA To: Xerox Corp.

The document was recorded in the United States Patent and Trademark Office at
Reel 020571, Frame 0851, or for which a copy thereof is attached.

☒ Additional documents in the chain of title are listed on a supplemental sheet(s).

☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Richard LaPeruta

Signature

July 8, 2010

Date

Richard LaPeruta (Reg. No. 51252)

Patent Counsel

Printed or Typed Name

Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



SUPPLEMENTAL SHEET

STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: THOMSON LICENSING Customer No. 24498 Docket No: XR940013

Application No./Patent No.: Appln. 08/453,732; Pat. 5,621,556 Filed/Issue Date: Filed May 30, 1995; Issued Apr. 15, 1997

Titled: METHOD OF MANUFACTURING ACTIVE MATRIX LCD USING FIVE MASKS

THOMSON LICENSING, a Corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

4. From: Xerox Corp. To: JP Morgan Chase Bank

The document was recorded in the United States Patent and Trademark Office at
Reel 015134 . Frame 0476 . or for which a copy thereof is attached.

5. From: Bank One, NA To: Xerox Corp.

The document was recorded in the United States Patent and Trademark Office at
Reel 020571 . Frame 0928 . or for which a copy thereof is attached.

6. From: Bank One, NA To: Xerox Corp.

The document was recorded in the United States Patent and Trademark Office at
Reel 020582 . Frame 0202 . or for which a copy thereof is attached.

7. From: JP Morgan Chase Bank To: Xerox Corp.

The document was recorded in the United States Patent and Trademark Office at
Reel 020540 . Frame 0483 . or for which a copy thereof is attached.

8. From: JP Morgan Chase Bank To: Xerox Corp.

The document was recorded in the United States Patent and Trademark Office at
Reel 021291 . Frame 0203 . or for which a copy thereof is attached.

9. From: Xerox Corp.; Palo Alto Research Center Inc. To: Thomson Licensing LLC

The document was recorded in the United States Patent and Trademark Office at
Reel 022575 . Frame 0761 . or for which a copy thereof is attached.

10. From: Thomson Licensing LLC To: Thomson Licensing

The document was recorded in the United States Patent and Trademark Office at
Reel 022575 . Frame 0746 . or for which a copy thereof is attached.



**POWER OF ATTORNEY
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We,

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France

do hereby grant

Robert D. Shedd
Vice President, U.S. Patent Operations
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Two Independence Way
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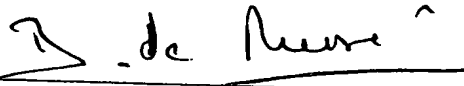
a revocable, non-exclusive and delegable power of attorney to act for us (including the signing of requisite documents) in proceedings concerning patents and applications for patents, including international and other multi-country patents and applications for patents, in our name in the Patent Offices in all countries worldwide from January 1, 2009.

DATED this 13 day of January, in the year 2009.

Signature:

Typed Name As Signed:

Title:


Béatrix de Russé
Executive Vice-President
Licensing, Research & Innovation



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THOMSON LICENSING**

THOMSON LICENSING
46, Quai A. Le Gallo
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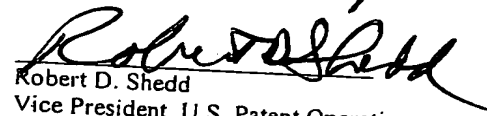
does hereby grant

Harvey D. Fried - Sr. Patent Counsel/Manager
Robert B. Levy - Sr. Patent Counsel/Manager
Frank Y. Liao - Sr. Patent Counsel/Manager
Reitseng Lin - Sr. Patent Counsel
Guy H. Eriksen - Sr. Patent Counsel
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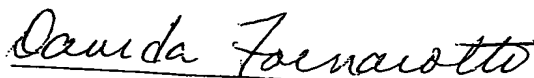
a revocable, non-exclusive and delegable power of attorney to act for us (including the signing of requisite documents) in proceedings concerning patents and applications for patents, including international and other multi-country patents and applications for patents, in our name in the Patent Offices in all countries worldwide from January 1, 2007.

DATED this 14 day of January, 2009.

SIGNED


Robert D. Shedd
Vice President, U.S. Patent Operations
Thomson Licensing LLC and
Attorney In Fact for
THOMSON LICENSING

WITNESS



PATENT ASSIGNMENT AGREEMENT

Between:

Thomson Licensing LLC, a Delaware, United States of America corporation having offices at Two Independence Way, Princeton, N.J. 08540, United States of America,

hereinafter referred to as the "Assignor".

and:

Thomson Licensing, a company organized and existing under the laws of France and having offices at 46, Quai Alphonse Le Gallo, 92100 Boulogne-Billancourt, France,

hereinafter referred to as the "Assignee".

WHEREAS Assignor is the owner or registered owner of certain patents and patent applications set forth on Exhibit 1 hereto (together with any and all related patents or patent applications that directly claim priority to the patents and patent applications set forth on Exhibit 1, including all corresponding patents and applications worldwide therefor and all patents (including utility models, and certificates of inventorship) resulting from reissues, continuations, continuations-in-part, divisions, renewals, reexaminations, substitutions and extensions of such patents or patent applications, all of the foregoing referred to as the "Purchased Patent Assets");

WHEREAS Assignor and Assignee have agreed, for good and valuable consideration, that all of Assignor's right, title and interest in and to the Purchased Patent Assets shall be assigned to Assignee;

THEREFORE, both parties hereby agree as follows:

ARTICLE 1

Assignor hereby assigns, conveys and transfers all right, title and interest in and to the Purchased Patent Assets.

Assignee hereby accepts such assignment, conveyance and transfer to it of the foregoing Purchased Patent Assets.

In consequence, Assignee shall have all right, title and interest in and to the foregoing Purchased Patent Assets, including the right to exploit, assign, and license them.

ARTICLE 2

Assignee will have the right to institute, continue or defend, any suit or action dealing with the foregoing Purchased Patent Assets.

To such effect, Assignee is subrogated to all Assignor's rights and actions, in substitution for those of Assignor, both with respect to claims and defenses.

ARTICLE 3

The foregoing assignment is concluded for good and valuable consideration, the sufficiency of which is expressly acknowledged by the parties

ARTICLE 4

The present Patent Assignment Agreement may be registered by or for the Assignee, at its expense, before the appropriate Patent Office(s).

ARTICLE 5

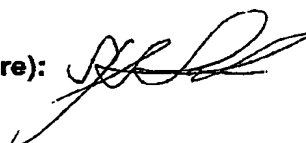
Once executed by Assignor and Assignee, the present Patent Assignment Agreement shall come into effect, as between the parties, retroactively as of October 1, 2008.

IN WITNESS WHEREOF, each of the parties hereto has caused the present Patent Assignment Agreement to be executed in two (2) original copies, one (1) for each party, by its duly authorized officer or representative.

ASSIGNOR

By (title and signature):

Date: 12/31/2008

 Stephen D. Amveth
President, Thomson Licensing LLC

ASSIGNEE

By (title and signature):

Date: December 31, 2008

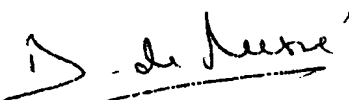
 B. de Heere
EVP Licensing, Research
and Innovation

EXHIBIT 1

	PATENT NUMBER/ APPLICATION NUMBER	JURISDICTION	TITLE
1	5081513	US	ELECTRONIC DEVICE WITH RECOVERY LAYER PROXIMATE TO ACTIVE LAYER
2	5153420	US	TIMING INDEPENDENT PIXEL-SCALE LIGHT SENSING APPARATUS
3	5166960	US	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
•	• 3199899	JPN	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
•	• 0570115	EPC (GB, FR, DE)	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
4	5204661	US	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
•	• 3251964	JPN	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
•	• 0490683	EPC (GB, FR, DE)	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
5	5315418	US	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY WITH LIGHT COUPLING LENS ARRAY DISPOSED ALONG THE RED-GREEN LIGHT PATH
6	5366926	US	LOW TEMPERATURE PROCESS FOR LASER DEHYDROGENATION AND CRYSTALLIZATION OF AMORPHOUS SILICON
7	5401982	US	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS
•	• 2140403	CAN	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS
•	• 0670604	EPC (GB, FR, DE)	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS

8	5442467	US	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS EFFICIENCY OF A LIQUID CRYSTAL DISPLAY EMPLOYING FIBEROPTIC FACEPLATE ELEMENTS
•	• 3578824	JPN	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS EFFICIENCY OF A LIQUID CRYSTAL DISPLAY EMPLOYING FIBEROPTIC FACEPLATE ELEMENTS
•	• 0674209	EPC (GB, FR, DE)	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS EFFICIENCY OF A LIQUID CRYSTAL DISPLAY EMPLOYING FIBEROPTIC FACEPLATE ELEMENTS
•	• 2138072	CAN	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS EFFICIENCY OF A LIQUID CRYSTAL DISPLAY EMPLOYING FIBEROPTIC FACEPLATE ELEMENTS
9	5491347	US	THIN-FILM STRUCTURE WITH DENSE ARRAY OF BINARY CONTROL UNITS FOR PRESENTING IMAGES
10	5504597	US	FULL COLOR DISPLAY WITH GRADIENT INDEX LENS ARRAY DISPOSED BETWEEN PHOSPHOR EMITTERS AND LIQUID CRYSTAL DISPLAY
11	5504598	US	LARGE SCREEN FULL COLOR DISPLAY WITH PLURAL ADJACENT DISPLAY PANELS AND ENLARGING GRADED INDEX LENS ARRAY
12	5518805	US	HILLOCK-FREE MULTILAYER METAL LINES FOR HIGH PERFORMANCE THIN FILM STRUCTURES
•	• 7095231	JPN (Pending)	HILLOCK-FREE MULTILAYER METAL LINES FOR HIGH PERFORMANCE THIN FILM STRUCTURES
•	• 0681328	EPC (GB, FR, DE)	HILLOCK-FREE MULTILAYER METAL LINES FOR HIGH PERFORMANCE THIN FILM STRUCTURES
13	5528082	US	THIN-FILM STRUCTURE WITH TAPERED FEATURE
14	5550656	US	FULL COLOR DISPLAY WITH PLURAL TWO-DIMENSIONAL PLANAR ARRAYS OF LENSLETS
15	5557534	US	FORMING ARRAY WITH METAL SCAN LINES TO CONTROL SEMICONDUCTOR GATE LINES
•	• 0721215	EPC (GB, FR, DE)	FORMING ARRAY WITH METAL SCAN

			LINES TO CONTROL SEMICONDUCTOR GATE LINES
16	5589847	US	SWITCHED CAPACITATOR ANALOG CIRCUITS USING POLYSILICON THIN FILM TECHNOLOGY
•	• 2049058	JPN	SWITCHED CAPACITATOR ANALOG CIRCUITS USING POLYSILICON THIN FILM TECHNOLOGY
•	• 0540163	EPC (GB, FR, DE)	SWITCHED CAPACITATOR ANALOG CIRCUITS USING POLYSILICON THIN FILM TECHNOLOGY
17	5600155	US	ARRAY WITH METAL SCAN LINES CONTROLLING SEMICONDUCTOR GATE LINES
•	• 0721213	EPC (GB, FR, DE)	ARRAY WITH METAL SCAN LINES CONTROLLING SEMICONDUCTOR GATE LINES
18	5608245	US	ARRAY ON SUBSTRATE WITH REPAIR LINE CROSSING LINES IN THE ARRAY
•	• 3938959	JPN	ARRAY ON SUBSTRATE WITH REPAIR LINE CROSSING LINES IN THE ARRAY
•	• 0780766	EPC (GB, FR, DE)	ARRAY ON SUBSTRATE WITH REPAIR LINE CROSSING LINES IN THE ARRAY
19	5608557	US	CIRCUITRY WITH GATE LINE CROSSING SEMICONDUCTOR LINE AT TWO OR MORE CHANNELS
•	• 3952517 • 2005-349299 (DIV)	JPN (Divisional Pending)	CIRCUITRY WITH GATE LINE CROSSING SEMICONDUCTOR LINE AT TWO OR MORE CHANNELS
•	• 0721214	EPC (GB, FR, DE)	CIRCUITRY WITH GATE LINE CROSSING SEMICONDUCTOR LINE AT TWO OR MORE CHANNELS
20	5621556	US	ACTIVE MATRIX LIQUID CRYSTAL DEVICE AND MANUFACTURING METHOD
•	• 0745886	EPC (GB, FR, DE)	ACTIVE MATRIX LIQUID CRYSTAL DEVICE AND MANUFACTURING METHOD
21	5642125	US	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY
•	• 3329887	JPN	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY
•	• 0579382	EPC (GB, FR, DE)	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY

22	5648674	US	ARRAY CIRCUITRY WITH CONDUCTIVE LINES, CONTACT LEADS, AND STORAGE CAPACITOR ELECTRODE ALL FORMED IN LAYER THAT INCLUDES HIGHLY CONDUCTIVE METAL
23	5654970	US	ARRAY WITH REDUNDANT INTEGRATED SELF-TESTING SCAN DRIVERS
•	• 3739874	JPN	ARRAY WITH REDUNDANT INTEGRATED SELF-TESTING SCAN DRIVERS
24	5682211	US	INTEGRATED DARK MATRIX FOR AN ACTIVE MATRIX LIQUID CRYSTAL DISPLAY WITH PIXEL ELECTRODES OVERLAPPING GATE AND DATA LINES
•	• 8127583	JPN (Pending)	INTEGRATED DARK MATRIX FOR AN ACTIVE MATRIX LIQUID CRYSTAL DISPLAY WITH PIXEL ELECTRODES OVERLAPPING GATE AND DATA LINES
•	• 96303898 9	EPC (GB, FR, DE) (Pending)	INTEGRATED DARK MATRIX FOR AN ACTIVE MATRIX LIQUID CRYSTAL DISPLAY WITH PIXEL ELECTRODES OVERLAPPING GATE AND DATA LINES
25	5693567	US	SEPARATELY ETCHING INSULATING LAYER FOR CONTACTS WITHIN ARRAY AND FOR PERIPHERAL PADS
26	5693983	US	THIN-FILM STRUCTURE WITH CONDUCTIVE MOLYBDENUM-CHROMIUM LINE
•	• 0680088	EPC (GB, FR, DE)	THIN-FILM STRUCTURE WITH CONDUCTIVE MOLYBDENUM-CHROMIUM LINE
27	5694053	US	DISPLAY MATRIX TESTER
28	5703382	US	ARRAY HAVING MULTIPLE CHANNEL STRUCTURES WITH CONTINUOUSLY DOPED INTERCHANNEL REGIONS
29	5703621	US	UNIVERSAL DISPLAY THAT PRESENTS ALL IMAGE TYPES WITH HIGH IMAGE FIDELITY
30	5707744	US	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES

•	• 8313160	JPN (Pending)	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES
•	• 0782178	EPC (GB, FR, DE)	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES
31	5717223	US	ARRAY WITH AMORPHOUS SILICON TFTS IN WHICH CHANNEL LEADS OVERLAP INSULATING REGION NO MORE THAN MAXIMUM OVERLAP
•	• 8335053	JPN (Pending)	ARRAY WITH AMORPHOUS SILICON TFTS IN WHICH CHANNEL LEADS OVERLAP INSULATING REGION NO MORE THAN MAXIMUM OVERLAP
•	• 0780909	EPC (GB, FR, DE)	ARRAY WITH AMORPHOUS SILICON TFTS IN WHICH CHANNEL LEADS OVERLAP INSULATING REGION NO MORE THAN MAXIMUM OVERLAP
32	5726730	US	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING REACTIVE LIQUID CRYSTALS AND POLYMERS
33	5731803	US	ARRAY WITH LIGHT ACTIVE UNITS SIZED TO ELIMINATE ARTIFACT FROM SIZE DIFFERENCE
•	• 96309251.5	EPC (GC, FR, DE) (Pending)	ARRAY WITH LIGHT ACTIVE UNITS SIZED TO ELIMINATE ARTIFACT FROM SIZE DIFFERENCE
34	5733841	US	BUFFERED SUBSTRATE FOR SEMICONDUCTOR DEVICES
•	• 9148652	JPN (Pending)	BUFFERED SUBSTRATE FOR SEMICONDUCTOR DEVICES
35	5733804	US	FABRICATING FULLY SELF-ALIGNED AMORPHOUS SILICON DEVICE
•	• 8335050	JPN (Pending)	FABRICATING FULLY SELF-ALIGNED AMORPHOUS SILICON DEVICE
•	• 0780892	EPC (GB, FR, DE)	FABRICATING FULLY SELF-ALIGNED AMORPHOUS SILICON DEVICE
36	5744202	US	ENHANCEMENT OF HYDROGENATION OF MATERIALS ENCAPSULATED BY AN OXIDE
•	• 9266852	JPN (Pending)	ENHANCEMENT OF HYDROGENATION OF MATERIALS ENCAPSULATED BY AN OXIDE
•	• 97307393.5	EPC (GB, FR, DE) (Pending)	ENHANCEMENT OF HYDROGENATION OF MATERIALS ENCAPSULATED BY AN OXIDE
37	5751390	US	ENHANCED OFF-AXIS VIEWING

			PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
•	• 9343367	JPN (Pending)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
•	• 97309846 0	EPC (GB, FR, DE) (Pending)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
38	5782665	US	FABRICATING ARRAY WITH STORAGE CAPACITOR BETWEEN CELL ELECTRODE AND DARK MATRIX
•	• 96309521.1	EPC (GB, FR, DE) (Pending)	FABRICATING ARRAY WITH STORAGE CAPACITOR BETWEEN CELL ELECTRODE AND DARK MATRIX
39	5831258	US	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 2204553	CAN	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 9217511	JPN (Pending)	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 97306165.8	EPC (GB, FR, DE) (Pending)	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
40	5867240	US	LIQUID CRYSTAL CELL CONSTRUCTED TO PRODUCE A HIGHLY ANISOTROPIC LIGHT DISTRIBUTION POSSESSING EXTREMELY HIGH CONTRAST AROUND A NARROW MERIDIAN
41	5867242	US	ELECTRICALLY ISOLATED PIXEL ELEMENT IN A LOW VOLTAGE ACTIVATED ACTIVE MATRIX LIQUID CRYSTAL DISPLAY AND METHOD

•	• 0679922	EPC (GB, FR, DE)	ELECTRICALLY ISOLATED PIXEL ELEMENT IN A LOW VOLTAGE ACTIVATED ACTIVE MATRIX LIQUID CRYSTAL DISPLAY AND METHOD
42	5871826	US	PROXIMITY LASER DOPING TECHNIQUE FOR ELECTRONIC MATERIALS
•	• 9132630	JPN (Pending)	PROXIMITY LASER DOPING TECHNIQUE FOR ELECTRONIC MATERIALS
43	5875012	US	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
•	• 10016301	JPN (Pending)	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
•	• 0856768	EPC (GB, FR, DE)	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
44	5893949	US	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES
45	5899711	US	METHOD FOR ENHANCING HYDROGENATION OF THIN FILM TRANSISTORS USING A METAL CAPPING LAYER AND METHOD FOR BATCH HYDROGENATION
46	5917464	US	COMBINATION OF 2-D DETECTOR ARRAY WITH DISPLAY FOR IMAGE PROCESSING
•	• 0708400	EPC (GB, FR, DE)	COMBINATION OF 2-D DETECTOR ARRAY WITH DISPLAY FOR IMAGE PROCESSING
47	5920401	US	COMPACT DOCUMENT IMAGER
•	• 6318590	JPN (Pending)	COMPACT DOCUMENT IMAGER
48	5928819	US	METHODS TO FABRICATE OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING REACTIVE LIQUID CRYSTALS AND POLYMERS
49	5956113	US	BISTABLE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
50	5959711	US	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE HAVING FIBER CLADDING MATERIAL

•	• 0747738	EPC (GB, FR, DE)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE HAVING FIBER CLADDING MATERIAL
51	5978063	US	SMART SPACERS FOR ACTIVE MATRIX LIQUID CRYSTAL PROJECTION LIGHT VALVES
•	• 10102810	JPN (Pending)	SMART SPACERS FOR ACTIVE MATRIX LIQUID CRYSTAL PROJECTION LIGHT VALVES
52	6019796	US	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
•	• 10298516	JPN (Pending)	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
•	• 0913860	EPC (GB, FR, DE)	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
53	6020223	US	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
54	6034756	US	LCDS WITH WIDE VIEWING ANGLE
•	• 10128444	JPN (Pending)	LCDS WITH WIDE VIEWING ANGLE
55	6040812	US	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
•	• 9155118	JPN (Pending)	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
•	• 97304178 3	EPC (GB, FR, DE) (Pending)	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
56	6078936	US	PRESENTING AN IMAGE ON A DISPLAY AS IT WOULD BE PRESENTED BY ANOTHER IMAGE OUTPUT DEVICE OR ON PRINTING CIRCUITRY
57	6107641	US	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
•	• 10249510	JPN (Pending)	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE

•	• 0902481	EPC (GB, FR, DE)	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
58	6130732	US	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
•	• 10016302	JPN (Pending)	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
•	• 0856765	EPC (GB, FR, DE)	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
59	6140668	US	SILICON STRUCTURES HAVING AN ABSORPTION LAYER
60	6160606	US	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
	• 10214520	JPN (Pending)	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
	• 98306165.6	EPC (GB, FR, DE) (Pending)	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
61	6166800	US	SOLID-STATE IMAGE CAPTURE SYSTEM INCLUDING H-PDLC COLOR SEPARATION ELEMENT
•	• 11372106	JPN (Pending)	SOLID-STATE IMAGE CAPTURE SYSTEM INCLUDING H-PDLC COLOR SEPARATION ELEMENT
62	6245602	US	TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
•	• 2000-352356	JPN (Pending)	TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
•	• 1102313	EPC (GB, FR, DE)	TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
63	6281891	US	DISPLAY WITH ARRAY AND MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG CONVERTER INTEGRATED CIRCUIT HAVING MANY OUTPUTS
•	• 3681470	JPN	DISPLAY WITH ARRAY AND MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG CONVERTER INTEGRATED CIRCUIT HAVING MANY OUTPUTS
•	• 207507	MX	DISPLAY WITH ARRAY AND MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG

			CONVERTER INTEGRATED CIRCUIT HAVING MANY OUTPUTS
64	6317189	US	HIGH-EFFICIENCY REFLECTIVE LIQUID CRYSTAL DISPLAY
•	• 11369152	JPN (Pending)	HIGH-EFFICIENCY REFLECTIVE LIQUID CRYSTAL DISPLAY
65	6339463	US	ENHANCED VIEWING ANGLE PERFORMANCE ON NON-POLARIZER BASED COLOR REFLECTIVE LIQUID CRYSTAL DISPLAY USING A FIBER- OPTIC FACEPLATE
•	• PI9800969-9	BR	ENHANCED VIEWING ANGLE PERFORMANCE ON NON-POLARIZER BASED COLOR REFLECTIVE LIQUID CRYSTAL DISPLAY USING A FIBER- OPTIC FACEPLATE
66	6408747	US	METHODS OF ENCAPSULATING CORES USING INK JETS OR FOGS
•	• 4108965	JPN	METHODS OF ENCAPSULATING CORES USING INK JETS OR FOGS
•	• 01127754.8	EPC (GB, FR, DE) (Pending)	METHODS OF ENCAPSULATING CORES USING INK JETS OR FOGS
67	6456273	US	FLAP ARRAY UNDER FLUIDIC AND ELECTRICAL CONTROL
68	6504175	US	HYBRID POLYCRYSTALLINE AND AMORPHOUS SILICON STRUCTURES ON A SHARED SUBSTRATE
69	6628447	US	ARRAY OF ROTATABLE SOLID ELEMENTS FOR COLOR DISPLAY
70	6677926	US	ELECTROPHORETIC DISPLAY DEVICE

EXHIBIT 3
PATENT ASSIGNMENT

WHEREAS, Xerox Corporation, a New York corporation with offices at 45 Glover Ave., Norwalk, CT 06856, and including its wholly owned subsidiary, Palo Alto Research Center Incorporated, a Delaware corporation with offices at 3333 Coyote Hill Rd., Palo Alto, CA 94304 (collectively "Xerox") is the sole and exclusive owner of those certain patents and patent applications set forth on **Attachment A** hereto (together with any and all related patents or patent applications that directly claim priority to the patents and patent applications set forth on Attachment A, including all foreign corresponding patents and applications therefor (in all countries) and all patents (including utility models, and certificates of inventorship) resulting from reissues, continuations, continuations-in-part, divisions, renewals, reexaminations, substitutions and extensions of such patents or patent applications referred to as the "Patent Assets"); and

WHEREAS, Thomson Licensing LLC a limited liability company with offices at 2 Independence Way, Princeton, New Jersey 08540 ("TL LLC") desires to acquire all right, title and interest in, to and under the said Patents;

NOW, THEREFORE, for good and valuable consideration the sufficiency of which is acknowledged by the parties:

Xerox does hereby irrevocably and perpetually assign, convey, and transfer to TL LLC, all of Xerox's right, title and interest throughout the world, in and to the Patent Assets, all of which are to be held and enjoyed by Purchaser for its own use and enjoyment, and for the use and enjoyment of its successors, assigns or other legal representatives, to the end of the term or terms for which said Patent Assets are or may be granted, reissued or extended as fully and entirely as the same would have been held and enjoyed by Xerox, if this assignment and sale had not been made; together with all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patent Assets, including, without limitation, all causes of action and other enforcement rights for (i) damages, (ii) injunctive relief, and (iii) any other remedies of any kind for past, current and future infringement, and all rights to collect royalties or other payments under or on account of any of the Patents, all for TL LLC's own use and behalf, and for the use and behalf of its successors, assigns or other legal representatives.

Xerox hereby authorizes and requests the Commissioner of Patents and Trademarks, or an equivalent officer in any jurisdiction in which a Patent may have issued, to issue any and all Letters Patent on said inventions to Purchaser as assignee of the entire interest, and hereby covenants that Xerox has full right to convey the entire interest herein assigned, and that, except as otherwise explicitly agreed and acknowledged in writing between the parties, Xerox has not executed, and will not execute, any agreements in conflict therewith.

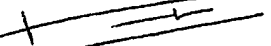
Signature Page Follows

FINAL

XEROX CORPORATION

By: SARIE VAN DER BEEK

Title: Xerox CTO

Signature: 

Date: 8/4/2008

THOMSON LICENSING LLC

By: DAVID T. SHONEMAN

Title: VP 

Signature: 

Date: 5 AUG 2008

PALO ALTO RESEARCH CENTER INCORPORATED

By: DAWN C. MATTHEW

Title: VP

Signature: 

Date: 8/4/08

**Signature Page to Patent Assignment from
Xerox Corporation and its wholly owned subsidiary Palo Alto Research Center
Incorporated
To
Thomson Licensing LLC**

ATTACHMENT A
TO EXHIBIT 3 PATENT ASSIGNMENT

	PATENT NUMBER/ APPLICATION NUMBER	JURISDICTION	TITLE
1	5081513	US	ELECTRONIC DEVICE WITH RECOVERY LAYER PROXIMATE TO ACTIVE LAYER
2	5153420	US	TIMING INDEPENDENT PIXEL-SCALE LIGHT SENSING APPARATUS
3	5166960	US	PARALLEL MULTI-PHASED A-Si SHIFT REGISTER FOR FAST ADDRESSING OF AN A-Si ARRAY
•	• 3199899	JPN	PARALLEL MULTI-PHASED A-Si SHIFT REGISTER FOR FAST ADDRESSING OF AN A-Si ARRAY
•	• 0570115	EPC (GB, FR, DE)	PARALLEL MULTI-PHASED A-Si SHIFT REGISTER FOR FAST ADDRESSING OF AN A-Si ARRAY
4	5204661	US	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
•	• 3251964	JPN	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
•	• 0490683	EPC (GB, FR, DE)	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
5	5315418	US	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY WITH LIGHT COUPLING LENS ARRAY DISPOSED ALONG THE RED-GREEN LIGHT PATH
6	5366926	US	LOW TEMPERATURE PROCESS FOR LASER DEHYDROGENATION AND CRYSTALLIZATION OF AMORPHOUS SILICON
7	5401982	US	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS
•	• 2140403	CAN	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS
•	• 0670604	EPC (GB, FR, DE)	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS

8	5442467	US	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS EFFICIENCY OF A LIQUID CRYSTAL DISPLAY EMPLOYING FIBEROPTIC FACEPLATE ELEMENTS
•	• 3578824	JPN	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS EFFICIENCY OF A LIQUID CRYSTAL DISPLAY EMPLOYING FIBEROPTIC FACEPLATE ELEMENTS
•	• 0674209	EPC (GB, FR, DE)	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS EFFICIENCY OF A LIQUID CRYSTAL DISPLAY EMPLOYING FIBEROPTIC FACEPLATE ELEMENTS
•	• 2138072	CAN	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS EFFICIENCY OF A LIQUID CRYSTAL DISPLAY EMPLOYING FIBEROPTIC FACEPLATE ELEMENTS
9	5491347	US	THIN-FILM STRUCTURE WITH DENSE ARRAY OF BINARY CONTROL UNITS FOR PRESENTING IMAGES
10	5504597	US	FULL COLOR DISPLAY WITH GRADIENT INDEX LENS ARRAY DISPOSED BETWEEN PHOSPHOR EMITTERS AND LIQUID CRYSTAL DISPLAY
11	5504598	US	LARGE SCREEN FULL COLOR DISPLAY WITH PLURAL ADJACENT DISPLAY PANELS AND ENLARGING GRADED INDEX LENS ARRAY
12	5518805	US	HILLOCK-FREE MULTILAYER METAL LINES FOR HIGH PERFORMANCE THIN FILM STRUCTURES
•	• 7095231	JPN (Pending)	HILLOCK-FREE MULTILAYER METAL LINES FOR HIGH PERFORMANCE THIN FILM STRUCTURES
•	• 0681328	EPC (GB, FR, DE)	HILLOCK-FREE MULTILAYER METAL LINES FOR HIGH PERFORMANCE THIN FILM STRUCTURES
13	5528082	US	THIN-FILM STRUCTURE WITH TAPERED FEATURE
14	5550656	US	FULL COLOR DISPLAY WITH PLURAL TWO-DIMENSIONAL PLANAR ARRAYS OF LENSLETS
15	5557534	US	FORMING ARRAY WITH METAL SCAN LINES TO CONTROL SEMICONDUCTOR GATE LINES
•	• 0721215	EPC (GB, FR,	FORMING ARRAY WITH METAL SCAN

		DE)	LINES TO CONTROL SEMICONDUCTOR GATE LINES
16	5589847	US	SWITCHED CAPACITATOR ANALOG CIRCUITS USING POLYSILICON THIN FILM TECHNOLOGY
•	• 2049058	JPN	SWITCHED CAPACITATOR ANALOG CIRCUITS USING POLYSILICON THIN FILM TECHNOLOGY
•	• 0540163	EPC (GB, FR, DE)	SWITCHED CAPACITATOR ANALOG CIRCUITS USING POLYSILICON THIN FILM TECHNOLOGY
17	5600155	US	ARRAY WITH METAL SCAN LINES CONTROLLING SEMICONDUCTOR GATE LINES
•	• 0721213	EPC (GB, FR, DE)	ARRAY WITH METAL SCAN LINES CONTROLLING SEMICONDUCTOR GATE LINES
18	5608245	US	ARRAY ON SUBSTRATE WITH REPAIR LINE CROSSING LINES IN THE ARRAY
•	• 3938959	JPN	ARRAY ON SUBSTRATE WITH REPAIR LINE CROSSING LINES IN THE ARRAY
•	• 0780766	EPC (GB, FR, DE)	ARRAY ON SUBSTRATE WITH REPAIR LINE CROSSING LINES IN THE ARRAY
19	5608557	US	CIRCUITRY WITH GATE LINE CROSSING SEMICONDUCTOR LINE AT TWO OR MORE CHANNELS
•	• 3952517 • 2005-349299 (DIV)	JPN (Divisional Pending)	CIRCUITRY WITH GATE LINE CROSSING SEMICONDUCTOR LINE AT TWO OR MORE CHANNELS
•	• 0721214	EPC (GB, FR, DE)	CIRCUITRY WITH GATE LINE CROSSING SEMICONDUCTOR LINE AT TWO OR MORE CHANNELS
20	5621556	US	ACTIVE MATRIX LIQUID CRYSTAL DEVICE AND MANUFACTURING METHOD
•	• 0745886	EPC (GB, FR, DE)	ACTIVE MATRIX LIQUID CRYSTAL DEVICE AND MANUFACTURING METHOD
21	5642125	US	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY
•	• 3329887	JPN	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY
•	• 0579382	EPC (GB, FR, DE)	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY

22	5648674	US	ARRAY CIRCUITRY WITH CONDUCTIVE LINES, CONTACT LEADS, AND STORAGE CAPACITOR ELECTRODE ALL FORMED IN LAYER THAT INCLUDES HIGHLY CONDUCTIVE METAL
23	5654970	US	ARRAY WITH REDUNDANT INTEGRATED SELF-TESTING SCAN DRIVERS
•	• 3739874	JPN	ARRAY WITH REDUNDANT INTEGRATED SELF-TESTING SCAN DRIVERS
24	5682211	US	INTEGRATED DARK MATRIX FOR AN ACTIVE MATRIX LIQUID CRYSTAL DISPLAY WITH PIXEL ELECTRODES OVERLAPPING GATE AND DATA LINES
•	• 8127583	JPN (Pending)	INTEGRATED DARK MATRIX FOR AN ACTIVE MATRIX LIQUID CRYSTAL DISPLAY WITH PIXEL ELECTRODES OVERLAPPING GATE AND DATA LINES
•	• 96303898.9	EPC (GB, FR, DE) (Pending)	INTEGRATED DARK MATRIX FOR AN ACTIVE MATRIX LIQUID CRYSTAL DISPLAY WITH PIXEL ELECTRODES OVERLAPPING GATE AND DATA LINES
25	5693567	US	SEPARATELY ETCHING INSULATING LAYER FOR CONTACTS WITHIN ARRAY AND FOR PERIPHERAL PADS
26	5693983	US	THIN-FILM STRUCTURE WITH CONDUCTIVE MOLYBDENUM-CHROMIUM LINE
•	• 0680088	EPC (GB, FR, DE)	THIN-FILM STRUCTURE WITH CONDUCTIVE MOLYBDENUM-CHROMIUM LINE
27	5694053	US	DISPLAY MATRIX TESTER
28	5703382	US	ARRAY HAVING MULTIPLE CHANNEL STRUCTURES WITH CONTINUOUSLY DOPED INTERCHANNEL REGIONS
29	5703621	US	UNIVERSAL DISPLAY THAT PRESENTS ALL IMAGE TYPES WITH HIGH IMAGE FIDELITY
30	5707744	US	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES

•	• 8313160	JPN (Pending)	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES
•	• 0782178	EPC (GB, FR, DE)	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES
31	5717223	US	ARRAY WITH AMORPHOUS SILICON TFTS IN WHICH CHANNEL LEADS OVERLAP INSULATING REGION NO MORE THAN MAXIMUM OVERLAP
•	• 8335053	JPN (Pending)	ARRAY WITH AMORPHOUS SILICON TFTS IN WHICH CHANNEL LEADS OVERLAP INSULATING REGION NO MORE THAN MAXIMUM OVERLAP
•	• 0780909	EPC (GB, FR, DE)	ARRAY WITH AMORPHOUS SILICON TFTS IN WHICH CHANNEL LEADS OVERLAP INSULATING REGION NO MORE THAN MAXIMUM OVERLAP
32	5726730	US	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING REACTIVE LIQUID CRYSTALS AND POLYMERS
33	5731803	US	ARRAY WITH LIGHT ACTIVE UNITS SIZED TO ELIMINATE ARTIFACT FROM SIZE DIFFERENCE
•	• 96309251.5	EPC (GC, FR, DE) (Pending)	ARRAY WITH LIGHT ACTIVE UNITS SIZED TO ELIMINATE ARTIFACT FROM SIZE DIFFERENCE
34	5733641	US	BUFFERED SUBSTRATE FOR SEMICONDUCTOR DEVICES
•	• 9148652	JPN (Pending)	BUFFERED SUBSTRATE FOR SEMICONDUCTOR DEVICES
35	5733804	US	FABRICATING FULLY SELF-ALIGNED AMORPHOUS SILICON DEVICE
•	• 8335050	JPN (Pending)	FABRICATING FULLY SELF-ALIGNED AMORPHOUS SILICON DEVICE
•	• 0780892	EPC (GB, FR, DE)	FABRICATING FULLY SELF-ALIGNED AMORPHOUS SILICON DEVICE
36	5744202	US	ENHANCEMENT OF HYDROGENATION OF MATERIALS ENCAPSULATED BY AN OXIDE
•	• 9266852	JPN (Pending)	ENHANCEMENT OF HYDROGENATION OF MATERIALS ENCAPSULATED BY AN OXIDE
•	• 97307393.5	EPC (GB, FR, DE) (Pending)	ENHANCEMENT OF HYDROGENATION OF MATERIALS ENCAPSULATED BY AN OXIDE
37	5751390	US	ENHANCED OFF-AXIS VIEWING

			PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
•	• 9343367	JPN (Pending)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
•	• 97309846.0	EPC (GB, FR, DE) (Pending)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
38	5782665	US	FABRICATING ARRAY WITH STORAGE CAPACITOR BETWEEN CELL ELECTRODE AND DARK MATRIX
•	• 96309521.1	EPC (GB, FR, DE) (Pending)	FABRICATING ARRAY WITH STORAGE CAPACITOR BETWEEN CELL ELECTRODE AND DARK MATRIX
39	5831258	US	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 2204553	CAN	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 9217511	JPN (Pending)	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 97306165.8	EPC (GB, FR, DE) (Pending)	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
40	5867240	US	LIQUID CRYSTAL CELL CONSTRUCTED TO PRODUCE A HIGHLY ANISOTROPIC LIGHT DISTRIBUTION POSSESSING EXTREMELY HIGH CONTRAST AROUND A NARROW MERIDIAN
41	5867242	US	ELECTRICALLY ISOLATED PIXEL ELEMENT IN A LOW VOLTAGE ACTIVATED ACTIVE MATRIX LIQUID CRYSTAL DISPLAY AND METHOD

•	• 0679922	EPC (GB, FR, DE)	ELECTRICALLY ISOLATED PIXEL ELEMENT IN A LOW VOLTAGE ACTIVATED ACTIVE MATRIX LIQUID CRYSTAL DISPLAY AND METHOD
42	5871826	US	PROXIMITY LASER DOPING TECHNIQUE FOR ELECTRONIC MATERIALS
•	• 9132630	JPN (Pending)	PROXIMITY LASER DOPING TECHNIQUE FOR ELECTRONIC MATERIALS
43	5875012	US	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
•	• 10016301	JPN (Pending)	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
•	• 0856768	EPC (GB, FR, DE)	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
44	5893949	US	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES
45	5899711	US	METHOD FOR ENHANCING HYDROGENATION OF THIN FILM TRANSISTORS USING A METAL CAPPING LAYER AND METHOD FOR BATCH HYDROGENATION
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•	• 0708400	EPC (GB, FR, DE)	COMBINATION OF 2-D DETECTOR ARRAY WITH DISPLAY FOR IMAGE PROCESSING
47	5920401	US	COMPACT DOCUMENT IMAGER
•	• 6318590	JPN (Pending)	COMPACT DOCUMENT IMAGER
48	5928819	US	METHODS TO FABRICATE OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING REACTIVE LIQUID CRYSTALS AND POLYMERS
49	5956113	US	BISTABLE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
50	5959711	US	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE HAVING FIBER CLADDING MATERIAL

•	• 0747738	EPC (GB, FR, DE)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE HAVING FIBER CLADDING MATERIAL
51	5978063	US	SMART SPACERS FOR ACTIVE MATRIX LIQUID CRYSTAL PROJECTION LIGHT VALVES
•	• 10102810	JPN (Pending)	SMART SPACERS FOR ACTIVE MATRIX LIQUID CRYSTAL PROJECTION LIGHT VALVES
52	6019796	US	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
•	• 10298516	JPN (Pending)	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
•	• 0913860	EPC (GB, FR, DE)	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
53	6020223	US	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
54	6034756	US	LCDS WITH WIDE VIEWING ANGLE
•	• 10128444	JPN (Pending)	LCDS WITH WIDE VIEWING ANGLE
55	6040812	US	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
•	• 9155118	JPN (Pending)	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
•	• 97304178.3	EPC (GB, FR, DE) (Pending)	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
56	6078936	US	PRESENTING AN IMAGE ON A DISPLAY AS IT WOULD BE PRESENTED BY ANOTHER IMAGE OUTPUT DEVICE OR ON PRINTING CIRCUITRY
57	6107641	US	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
•	• 10249510	JPN (Pending)	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE

•	• 0902481	EPC (GB, FR, DE)	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
58	6130732	US	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
•	• 10016302	JPN (Pending)	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
•	• 0856765	EPC (GB, FR, DE)	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
59	6140668	US	SILICON STRUCTURES HAVING AN ABSORPTION LAYER
60	6160606	US	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
	• 10214520	JPN (Pending)	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
	• 98306165.6	EPC (GB, FR, DE) (Pending)	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
61	6166800	US	SOLID-STATE IMAGE CAPTURE SYSTEM INCLUDING H-PDLC COLOR SEPARATION ELEMENT
•	• 11372106	JPN (Pending)	SOLID-STATE IMAGE CAPTURE SYSTEM INCLUDING H-PDLC COLOR SEPARATION ELEMENT
62	6245602	US	TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
•	• 2000-35235 6	JPN (Pending)	TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
•	• 1102313	EPC (GB, FR, DE)	TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
63	6281891	US	DISPLAY WITH ARRAY AND MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG CONVERTER INTEGRATED CIRCUIT HAVING MANY OUTPUTS
•	• 3681470	JPN	DISPLAY WITH ARRAY AND MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG CONVERTER INTEGRATED CIRCUIT HAVING MANY OUTPUTS
•	• 207507	MX	DISPLAY WITH ARRAY AND

			MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG CONVERTER INTEGRATED CIRCUIT HAVING MANY OUTPUTS
64	6317189	US	HIGH-EFFICIENCY REFLECTIVE LIQUID CRYSTAL DISPLAY
•	• 11369152	JPN (Pending)	HIGH-EFFICIENCY REFLECTIVE LIQUID CRYSTAL DISPLAY
65	6339463	US	ENHANCED VIEWING ANGLE PERFORMANCE ON NON-POLARIZER BASED COLOR REFLECTIVE LIQUID CRYSTAL DISPLAY USING A FIBER-OPTIC FACEPLATE
•	• PI9800969-9	BR	ENHANCED VIEWING ANGLE PERFORMANCE ON NON-POLARIZER BASED COLOR REFLECTIVE LIQUID CRYSTAL DISPLAY USING A FIBER-OPTIC FACEPLATE
66	6406747	US	METHODS OF ENCAPSULATING CORES USING INK JETS OR FOGS
•	• 4108965	JPN	METHODS OF ENCAPSULATING CORES USING INK JETS OR FOGS
•	• 01127754.8	EPC (GB, FR, DE) (Pending)	METHODS OF ENCAPSULATING CORES USING INK JETS OR FOGS
67	6456273	US	FLAP ARRAY UNDER FLUIDIC AND ELECTRICAL CONTROL
68	6504175	US	HYBRID POLYCRYSTALLINE AND AMORPHOUS SILICON STRUCTURES ON A SHARED SUBSTRATE
69	6628447	US	ARRAY OF ROTATABLE SOLID ELEMENTS FOR COLOR DISPLAY
70	6677926	US	ELECTROPHORETIC DISPLAY DEVICE